

WEST Search History

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DATE: Thursday, October 13, 2005

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=JPAB; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L16	L15 and (chip\$ or IC\$)	50
<input type="checkbox"/>	L15	L14 and (wiring\$ or line\$)	58
<input type="checkbox"/>	L14	L12 and (side or surface) near3 substrate	110
<input type="checkbox"/>	L13	L12 and (side or surface)	193
<input type="checkbox"/>	L12	(multichip or multi-chip) and substrate	388
<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L11	L10 and wiring substrate	26
<input type="checkbox"/>	L10	L9 and multi-chip module	123
<input type="checkbox"/>	L9	L8 and (signal\$ or line\$)	225
<input type="checkbox"/>	L8	L7 and module\$	226
<input type="checkbox"/>	L7	L6 and wiring\$	267
<input type="checkbox"/>	L6	L5 and (one or same\$ or other or first or second) near4 (side or surface) near4 substrate	514
<input type="checkbox"/>	L5	L4 and (chip\$ or IC\$)	2543
<input type="checkbox"/>	L4	L3 and data	2638
<input type="checkbox"/>	L3	L2 and (multichip or multi-chip)	4357
<input type="checkbox"/>	L2	L1 and substrate	147811
<input type="checkbox"/>	L1	memory	786827

END OF SEARCH HISTORY